



Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty Docket No. ANDIP006 Applicant: Di Benedetto et al. Filing Date December 21, 2001	Application No.: 10/026,311 Group 2633 2143
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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub- class	Filing Date
<i>AP</i>	A	6,128,750	10/3/00	Espy et al.			
	B						
	C						
	D						
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Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub- class	Translation	
							Yes	No
<i>AP</i>	J	01/35244 A1	5/17/01	WO				
	K							
	L							
	M							
	N							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
<i>AP</i>	O	Fourie et al., "METHOD FOR INTRODUCING SWITCHED VIRTUAL CONNECTION CALL REDUNDANCY IN ASYNCHRONOUS TRANSFER MODE NETWORKS", United States Patent Application Publication No.: US 2001/0002193 A1, Pub. Date: May 31, 2001.
<i>AP</i>	P	Cantwell et al., "METHOD AND APPARATUS FOR IMPARTING FAULT TOLERANCE IN A SWITCH OR THE LIKE", United States Patent Application Publication No.: US 2002/0019958 A1, Pub. Date: Feb 14, 2002
<i>AP</i>	Q	International Search Report, Application No. PCU/US02/40512, Mailed 5/19/03; 4 pages.
Examiner <i>[Signature]</i>		Date Considered <i>Feb-2-05</i>

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Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
AB	O	Marco Di Benedetto, Mrinal Baruah, Chengelpet V. Ramesh, Alagu Annaamalai, Ramana Mellacheruvu, Sridhar K. Aswathnarayan, Hei T. Fung, And Umesh Mahajan, "High Availability Architecture for Network Devices", U.S. Provisional Application Serial Number 09/714,246, filed November 16, 2000.
	P	
	Q	
Examiner	Date Considered	

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.